

Docket No.: 18865K-014600US

Client Ref. No.: FS-18850-US

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Examiner:

Monica Lewis

Soo-seong Kim et al.

Art Unit:

2822

Application No.: 10/807,917

Filed: March 23, 2004

RESPONSE TO RESTRICTION REQUIREMENT

For: MOS-GATED TRANSISTOR

WITH IMPROVED UIS CAPABILITY

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

December 15, 2005

Sir:

In response to the Restriction Requirement dated August 25, 2005, Applicants elect the claims of **Group I**, claims 1-21, drawn to a semiconductor device comprising a MOS-gated transistor, without traverse.

## CONCLUSION

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at (650) 326-2400.

Respectfully submitted,

Reg. No. 45,068

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